

PATENT APPLICATION

Attorney Docket: 54486-D1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED
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Applicant: Patti
Serial No.: 10/658,132
Filed: 9/8/2003
For: Interlocking Conductor Method for
Bonding Wafers to Produce Stacked
Integrated Circuits
Group Art Unit: 2823

Examiner: Toledo, Fernando

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

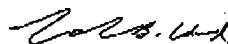
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MAR 18 2004

TRANSMITTAL OF RESPONSE TO RESTRICTION REQUIREMENT

In response to the office action dated 3/8/04, Applicants submit herewith a Response to the Restriction Requirement in the above identified patent application.

Applicants believe that no fee is required. However, the Commissioner is hereby authorized to charge any fees which may be required in this application under 37 C.F.R. Section 1.16-1.17 during its entire pendency, or credit any overpayment, to Deposit Account No. 23-0424. This sheet is filed in duplicate.



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Date:

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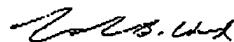
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RESPONSE TO RESTRICTION REQUIREMENT

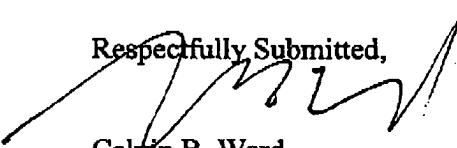
Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Sir:

In reply to the Office Action dated 3/8/2004, Applicant elects Group I, claims 1-12.

I hereby certify that this paper is being sent by FAX to 703-872-9306.

Respectfully Submitted,


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